

GPTP-T1XXXX

3.125Gbps PIN-TIA Receiver With Pigtail Modules



Features

- ◆ High sensitivity
- ◆ Differential ended output
- ◆ Single +3.3 operation
- ◆ Trans-impedance amplifier with AGC
- ◆ RoHS Compliant Products Available

Applications

- ◆ 3.125Gbps application
- ◆ SDH/SONET application

General

GPTP-913XXX Series is a 4 pin or 5 pin PIN-TIA with pigtail operating on 3.125Gbps. It provides high sensitivity with AGC, 100ohm differential outputs and the 5 pin PIN-TIA provides a monitor pin. A photodiode is mounted into a low capacitance coaxial package integrated with a pigtail.

Ordering Information (Standard version^{*Note1})

Part No.	Wavelength (nm)	Voltage (V)	Package	Pin Type	Connector
GPTP-T13CAF	1100~1650	3.3	A	C	FC/PC
GPTP-T13CBSA	1100~1650	3.3	B	C	SC/APC
GPTP-T13CCT	1100~1650	3.3	C	C	ST/PC
GPTP-T13CDS	1100~1650	3.3	D	C	SC/PC
GPTP-T13CES	1100~1650	3.3	E	C	SC/PC

*Note1: For more ordering information, please refer the nomenclature and contact EPOTOLINK sales.

Absolute maximum ratings^{*Note2}

Parameter	Min	Typ.	Max	Unit
Storage Temperature	-40	25	85	□
Operating Temperature	-40	25	85	□
TIA Supply Voltage	3.1	3.3	3.5	V
Operation Relative Humidity	-		85	%
Soldering Temperature / Time	-		260/10	°C/S

*Note2: Exceeding any one of these values may destroy the device permanently.

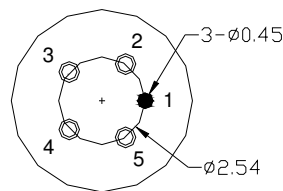
Electrical and optical characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Condition
Operating Wavelength	λ	1100		1650	nm	
Supply Current	I _{cc}		40	60	mA	No Loads
Saturation Power	P _{sat}	-3	0	-	dBm	@ 1310nm
Small-Signal Bandwidth	BW		3.125		GHz	
Low-Frequency Cut off	LF			5	kHz	
Sensitivity			-21	-19	dBm	1310nm, 3.125Gbps, ER=10@ PRBS= 2 ²³ -1, BER=1E-10
Single Ended Output Impedance	R	40	50	60	Ω	
Rise /FallTime	T		0.15	0.2	ns	20~80%

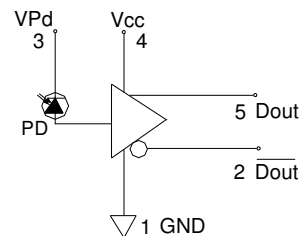
Pin

*Note3

TYPE: D



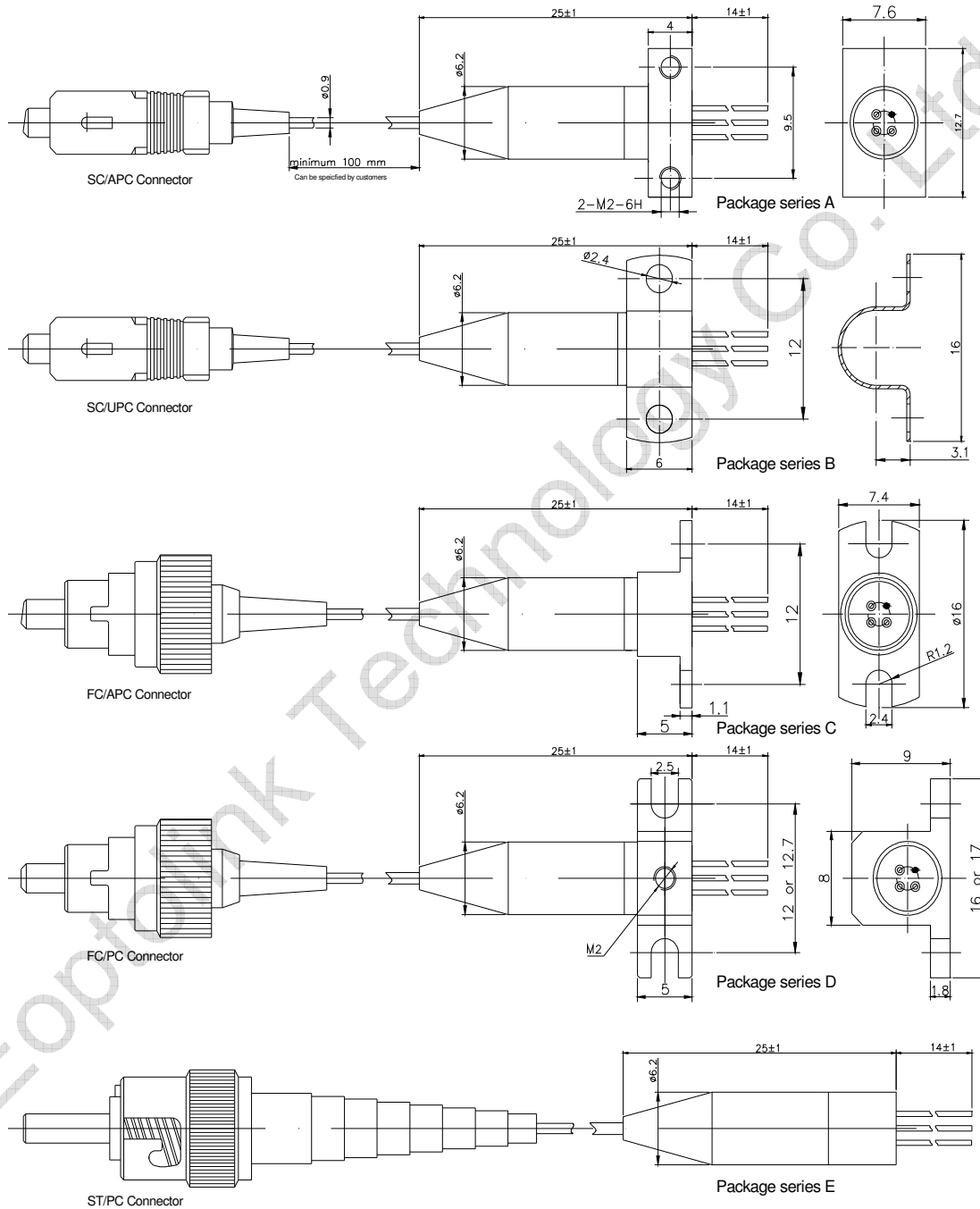
Assignment



PIN-TIA-pin-D

Note3: Other Pin type can be customized.

Pigtail Package dimension*Note4, 5, 6



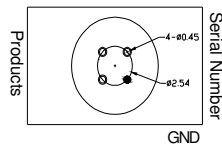
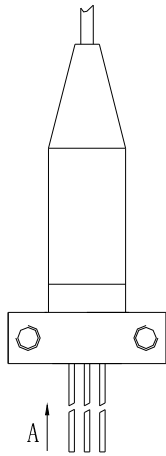
*Note4: PIN direction and laser mark can be customized. Pigtail is standard SM fiber; the length also can be customized.

*Note5: For the package series D, the clamping rings dimensions (A) and drill size (B) are can be selected. The following types can be available. Please designate the detailed type while ordering the package series D.

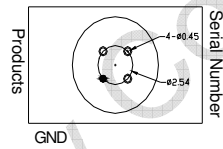
	A(mm)	B(mm)
D	16	12
D-S	17	12.7

*Note6: For the package series B, the fix card is fixed by customer self. For the detailed information of fix card of A, C, D package series, please refers the following graphs.

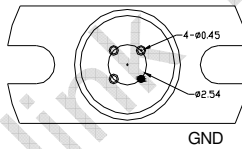
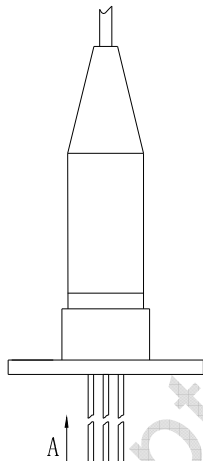
The direction of fix card



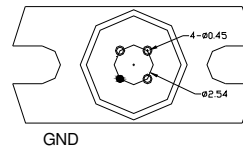
Direction A view
PPT4A-1(DEFAULT)



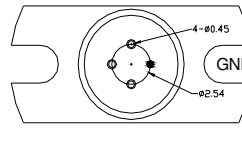
Direction A view
PPT4A-2



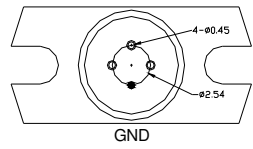
Direction A view
PPT4C-1(DEFAULT)



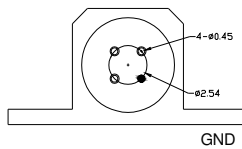
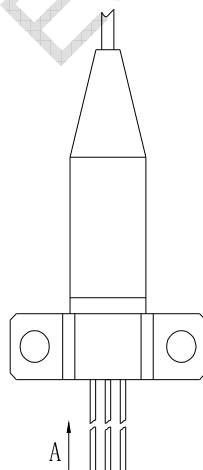
Direction A view
PPT4C-2



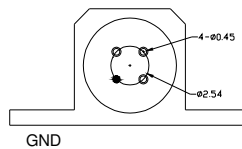
Direction A view
PPT4C-3



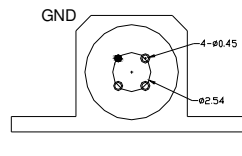
Direction A view
PPT4C-4



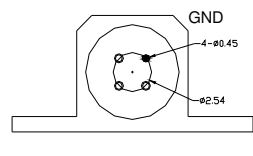
Direction A view
PPT4D-1(DEFAULT)



Direction A view
PPT4D-2

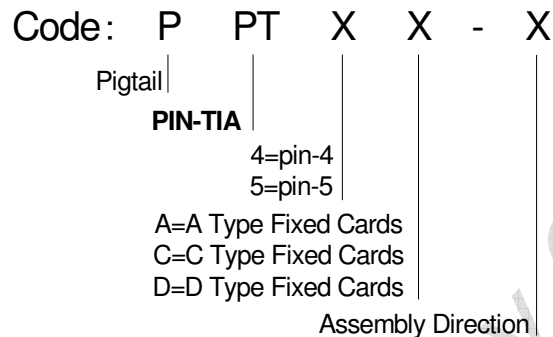


Direction A view
PPT4D-3



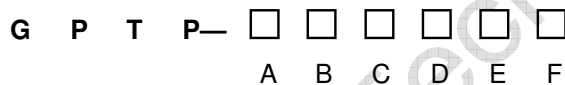
Direction A view
PPT4D-4

Nomenclature of assembly direction ^{*Note7}



*Note7: Please designate the code of assembly direction.

Nomenclature



A	Date rate	T=3.125Gbps					
B	Wavelength	1=1100~1650nm					
C	Voltage	3=3.3V					
D	Pin Type	C= pin-C					
E	Package series	A	B	C	D	E	
F	Connector	F=FC/PC		S=SC/PC		T=ST/PC	SA=SC/APC
		FA=FC/APC		L=LC/PC		Blank=None	

Precaution

- (1) The modules should be handled in the same manner as ordinary semiconductor devices to prevent the electro-static damages. For safe keeping and carrying, the modules should be packaged with ESD proof material. To assemble the modules on PCB, the workbench, the soldering iron and the human body should be grounded.
- (2) Please pay special attention to the atmosphere condition because the dew on the module may cause some electrical damages.

(3) Under such a strong vibration environment as in automobile, the performance and reliability are not guarant.

Obtaining Document

You can visit our website:

<http://www.eoptolink.com>

Or contact Eoptolink Technology Inc., Ltd. listed at the end of the documentation to get the latest documentation.

Revision History

Verision	Initiated	Reviewed	Approved	Release Date
Va-4	Zore.Zhao	Kelly		2009-12-23

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