

EOLS-BI1303-80 Series

EOLS-BI1503-80 Series

Single-Mode 155Mbps SDH /SONET
Simplex LC Single-Fiber SFP Transceiver
RoHS6 Compliant



Features

- ◆ Support 155Mbps data links
- ◆ A type: 1310nm DFB Tx/1550nmRx
B type: 1550nm DFB Tx/1310nmRx
- ◆ 80Km with 9/125 μm SMF
- ◆ Single 3.3V Power supply and TTL Logic Interface
- ◆ Hot-Pluggable SFP Footprint Simplex LC Connector Interface
- ◆ Class 1 FDA and IEC60825-1 laser safety compliant
- ◆ Operating Case Temperature
Standard: 0°C~+70°C
Industrial: -40°C~+85°C
- ◆ Compliant with SFP-MSA
- ◆ Compliant with SFF-8472

Applications

- ◆ SONET OC-3 / SDH STM-1
- ◆ WDM Fast Ethernet Links

Ordering information

Part No.	Data Rate	Wavelength	Interface	Temp.	DDMI
EOLS-BI1303-80 ^{*(note1)}	100M~155Mbps	1310nm	SC	Standard	NO
EOLS-BI1503-80 ^{*(note1)}	100M~155Mbps	1550nm	SC	Standard	NO
EOLS-BI1303-80I	100M~155Mbps	1310nm	SC	Industrial	NO
EOLS-BI1503-80I	100M~155Mbps	1550nm	SC	Industrial	NO
EOLS-BI1303-80D	100M~155Mbps	1310nm	SC	Standard	YES
EOLS-BI1503-80D	100M~155Mbps	1550nm	SC	Standard	YES
EOLS-BI1303-80DI	100M~155Mbps	1310nm	SC	Industrial	YES
EOLS-BI1503-80DI	100M~155Mbps	1550nm	SC	Industrial	YES
EOLS-BI1303-80L ^{*(note1)}	100M~155Mbps	1310nm	LC	Standard	NO

EOLS-BI1503-80L ^{*(note1)}	100M~155Mbps	1550nm	LC	Standard	NO
EOLS-BI1303-80IL	100M~155Mbps	1310nm	LC	Industrial	NO
EOLS-BI1503-80IL	100M~155Mbps	1550nm	LC	Industrial	NO
EOLS-BI1303-80DL	100M~155Mbps	1310nm	LC	Standard	YES
EOLS-BI1503-80DL	100M~155Mbps	1550nm	LC	Standard	YES
EOLS-BI1303-80DIL	100M~155Mbps	1310nm	LC	Industrial	YES
EOLS-BI1503-80DIL	100M~155Mbps	1550nm	LC	Industrial	YES

Note1: Standard version

Regulatory Compliance

Feature	Standard	Performance
Electrostatic Discharge (ESD) to the Electrical Pins	MIL-STD-883G Method 3015.7	Class 1C (>1000 V)
Electrostatic Discharge to the enclosure	EN 55024:1998+A1+A2 IEC-61000-4-2 GR-1089-CORE	Compatible with standards
Electromagnetic Interference (EMI)	FCC Part 15 Class B EN55022:2006 CISPR 22B :2006 VCCI Class B	Compatible with standards Noise frequency range: 30 MHz to 6 GHz. Good system EMI design practice required to achieve Class B margins. System margins are dependent on customer host board and chassis design.
Immunity	EN 55024:1998+A1+A2 IEC 61000-4-3	Compatible with standards. 1kHz sine-wave, 80% AM, from 80 MHz to 1 GHz. No effect on transmitter/receiver performance is detectable between these limits.
Laser Eye Safety	FDA 21CFR 1040.10 and 1040.11 EN (IEC) 60825-1:2007 EN (IEC) 60825-2:2004+A1	CDRH compliant and Class I laser product. TüV Certificate No. 50135086
Component Recognition	UL and CUL EN60950-1:2006	UL file E317337 TüV Certificate No. 50135086 (CB scheme)
RoHS6	2002/95/EC 4.1&4.2 2005/747/EC 5&7&13	Compliant with standards ^{*note2}

Note2: For update of the equipments and strict control of raw materials, EOPTOLINK has the ability to supply the customized products since Jan 1th, 2007, which meet the requirements of RoHS6 (Restrictions on use of certain Hazardous Substances) of European Union.

In light of item 5 in RoHS exemption list of RoHS Directive 2002/95/EC, Item 5: Lead in glass of cathode ray tubes, electronic components and fluorescent tubes.

In light of item 13 in RoHS exemption list of RoHS Directive 2005/747/EC, Item13: Lead and cadmium in optical and filter glass. The three exemptions are being concerned for Eoptolink's transceivers, because Eoptolink's transceivers use glass, which may contain Pb, for components such as lenses, windows, isolators, and other electronic components.

Product Description

The EOLS-BI1X03-80X series is small form factor pluggable module for IEEE 802.3ah 1000BASE-BX and OC-3/STM-1 SONET/SDH single fiber communications by using 1310 nm/1550nm transmitter and 1550nm/1310nm receiver. It is with the SFP 20-pin connector to allow hot plug capability.

The transmitter section uses a multiple quantum well A type/ B type laser and is a class 1 laser compliant according to International Safety Standard IEC 60825. The receiver section uses an integrated A type/ B type detector preamplifier (IDP) mounted in an optical header and a limiting post-amplifier IC.

The EOLS-BI1X03-80DIL series are designed to be compliant with SFF-8472.

Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit
Storage Temperature	T_S	-40	+85	°C
Supply Voltage	V_{CC}	-0.5	3.6	V
Operating Relative Humidity		-	95	%

*Exceeding any one of these values may destroy the device immediately.

Recommended Operating Conditions

Parameter	Symbol	Min.	Typical	Max.	Unit
Operating Case Temperature	T_A	EOLS-BIXX03-80	0	+70	°C
		EOLS-BIXX03-80I	-40	+85	
Power Supply Voltage	V_{CC}	3.15	3.3	3.45	V
Power Supply Current	I_{CC}			300	mA
Data Rate	OC-3		155		Mbps
	100M		100		Mbps

Performance Specifications - Electrical

Parameter	Symbol	Min.	Typ.	Max	Unit	Notes
Transmitter						
LVPECL	V_{in}	400		2000	mVpp	AC coupled

Inputs(Differential)							inputs ^{*(note5)}
Input Impedance (Differential)		Zin	85	100	115	ohms	Rin > 100 kohms @ DC
Tx_Dis	Disable		2		Vcc	V	
	Enable		0		0.8		
Tx_FAULT T	Fault		2		Vcc+0.3	V	
	normal		0		0.5		
Receiver							
LVPECL Outputs (Differential)		Vout	370		2000	mVpp	AC coupled outputs ^{*(note5)}
Output Impedance (Differential)		Zout	85	100	115	ohms	
Rx_LOS	LOS		2		Vcc+0.3	V	
	normal		0		0.8	V	
MOD_DEF (0:2)		VoH	2.5			V	With Serial ID
		VoL	0		0.5	V	

Performance Specifications - Optical

(EOLS-BI1303-80, 1310nm DFB and PIN, 80km)

Parameter	Symbol	Min.	Typical	Max.	Unit	
9µm Core Diameter SMF	L		80		Km	
Data Rate			100/155		Mbps	
Transmitter						
Centre Wavelength	λ_c	1290	1310	1330	Nm	
Spectral Width (-20dB)	$\Delta\lambda$			1	Nm	
Average Output Power ^{*(note3)}	Pout	-2		+3	dBm	
Extinction Ratio ^{*(note4)}	ER	9			dB	
Rise/Fall Time(20%~80%)	tr/tf			2	Ns	
Output Optical Eye ^{*(note4)}	IUT-T G.957 Compliant ^{*(note7)}					
TX_Disable Assert Time	t_off			10	Us	
Pout@TX Disable Asserted	P0ut			-45	dBm	
Receiver						
Centre Wavelength	λ_c	1500	1550	1600	Nm	
Receiver Sensitivity ^{*(note6)}	OC-3	Pmax, EOL			-34	dBm
	100M				-34	dBm
Receiver Overload	Pmin	-10			dBm	
Optical Path Penalty				1	dB	
LOS De-Assert	LOSD			-35	dBm	
LOS Assert	LOSA	-42			dBm	
LOS Hysteresis ^{*(note8)}		0.5			dB	

(EOLS-BI1503-80DIL, 1550nm DFB and PIN, 80km)

Parameter	Symbol	Min.	Typical	Max.	Unit
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9µm Core Diameter SMF		L		80		Km
Data Rate				100/155		Mbps
Transmitter						
Centre Wavelength		λ_c	1530	1550	1570	Nm
Spectral Width (-20dB)		$\Delta\lambda$			1	Nm
Average Output Power*(note3)		Pout	-2		+3	dBm
Extinction Ratio*(note4)		ER	9			dB
Side Mode Suppression Ratio		SMSR	30			dB
Rise/Fall Time(20%~80%)		tr/ff			2	Ns
Output Optical Eye*(note4)		IUT-T G.957 Compliant*(note7)				
TX_Disable Assert Time		t_off			10	Us
Receiver						
Centre Wavelength		λ_c	1260		1360	nm
Receiver Sensitivity @ EOL *(note6)	OC-3	Pmin, EOL			-34	dBm
	100M				-34	dBm
Receiver Overload		Pmax	-10			dBm
Return Loss			12			dB
Optical Path Penalty					1	dB
LOS De-Assert		LOSD			-35	dBm
LOS Assert		LOSA	-45			dBm
LOS Hysteresis*(note8)			0.5			dB

Note3: Output is coupled into a 9/125µm single-mode fiber.

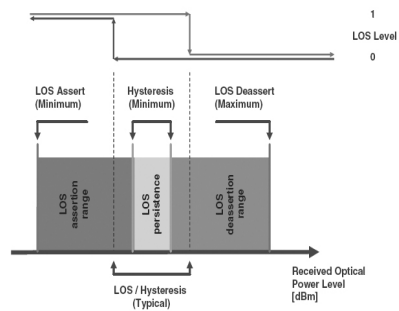
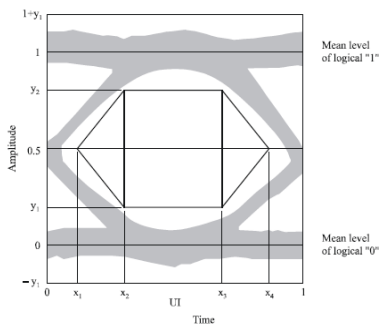
Note4: Filtered, measured with a PRBS 2²³-1 test pattern @155Mbps

Note5: LVPECL logic, internally AC coupled.

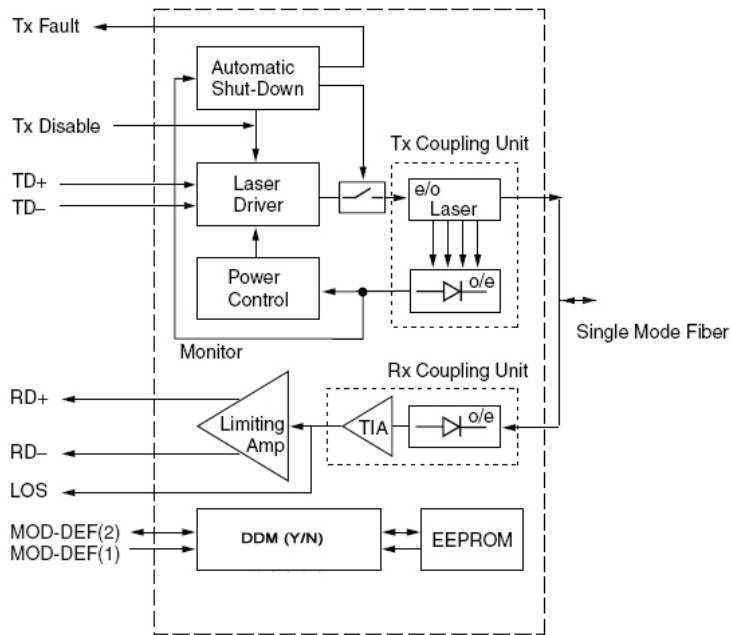
Note6: Minimum average optical power measured at the BER less than 1E-10 with a 2²³-1 PRBS and ER=9 dB.

Note7: Eye pattern mask

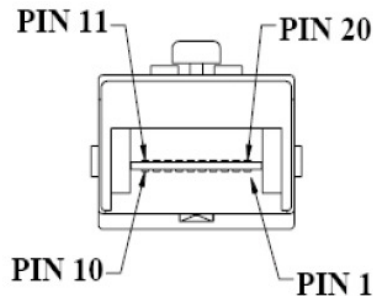
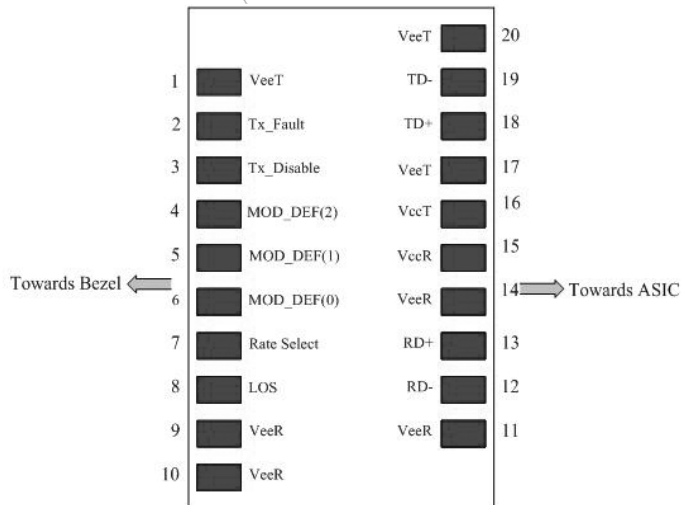
Note8: LOS Hysteresis



Functional Description of Transceiver



SFP Transceiver Electrical Pad Layout



Pin Function Definitions

Pin Num.	Name	FUNCTION	Plug Seq.	Notes
1	VeeT	Transmitter Ground	1	5)
2	TX Fault	Transmitter Fault Indication	3	1)
3	TX Disable	Transmitter Disable	3	2), Module disables on high or open
4	MOD-DEF2	Module Definition 2	3	3), Data line for Serial ID.
5	MOD-DEF1	Module Definition 1	3	3), Clock line for Serial ID.
6	MOD-DEF0	Module Definition 0	3	3), Grounded within the module.
7	Rate Select	Not Connect	3	Function not available
8	LOS	Loss of Signal	3	4)
9	VeeR	Receiver Ground	1	5)
10	VeeR	Receiver Ground	1	5)
11	VeeR	Receiver Ground	1	5)
12	RD-	Inv. Received Data Out	3	6)
13	RD+	Received Data Out	3	7)
14	VeeR	Receiver Ground	1	5)
15	VccR	Receiver Power	2	3.3 ± 5%, 7)
16	VccT	Transmitter Power	2	3.3 ± 5%, 7)
17	VeeT	Transmitter Ground	1	5)
18	TD+	Transmit Data In	3	8)
19	TD-	Inv. Transmit Data In	3	8)
20	VeeT	Transmitter Ground	1	5)

Notes:

1) TX Fault is an open collector/drain output, which should be pulled up with a 4.7K – 10KΩ resistor on the host board. Pull up voltage between 2.0V and VccT, R+0.3V. When high, output indicates a laser fault of some kind. Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.

2) TX disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a 4.7 – 10 K Ω resistor. Its states are:

Low (0 – 0.8V): Transmitter on

(>0.8, < 2.0V): Undefined

High (2.0 – 3.465V): Transmitter Disabled

Open: Transmitter Disabled

3) Mod-Def 0,1,2. These are the module definition pins. They should be pulled up with a 4.7K – 10KΩ resistor on the host board. The pull-up voltage shall be VccT or VccR (see Section IV for

further details). Mod-Def 0 is grounded by the module to indicate that the module is present Mod-Def 1 is the clock line of two wire serial interface for serial ID Mod-Def 2 is the data line of two wire serial interface for serial ID

4) LOS (Loss of Signal) is an open collector/drain output, which should be pulled up with a 4.7K – 10KΩ resistor. Pull up voltage between 2.0V and VccT, R+0.3V. When high, this output indicates the received optical power is below the worst-case receiver sensitivity (as defined by the standard in use). Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.

5) VeeR and VeeT may be internally connected within the SFP module.

6) RD-/+ : These are the differential receiver outputs. They are AC coupled 100Ω differential lines which should be terminated with 100Ω (differential) at the user SERDES. The AC coupling is done inside the module and is thus not required on the host board. The voltage swing on these lines will be between 370 and 2000 mV differential (185 –1000 mV single ended) when properly terminated.

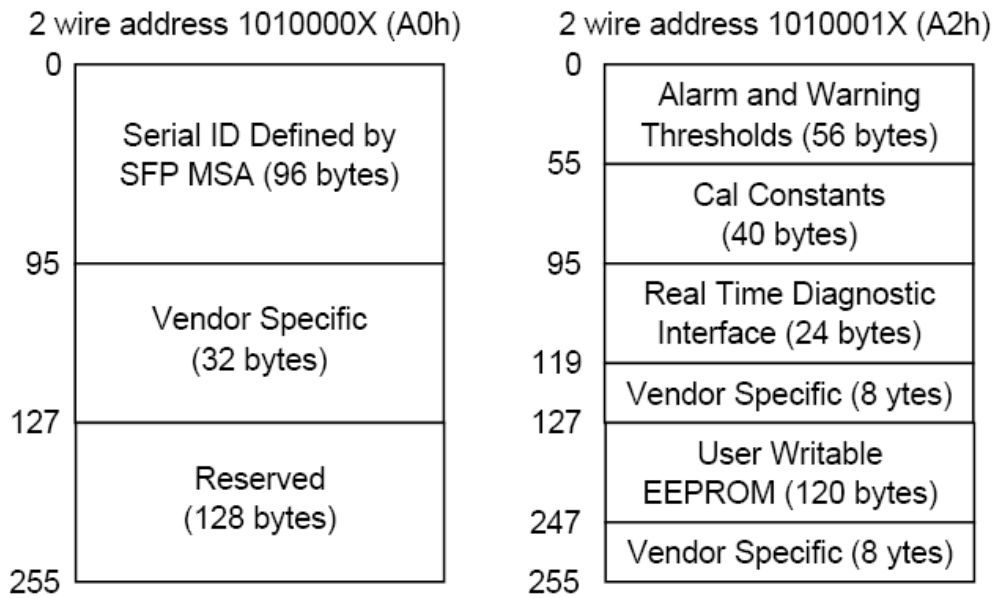
7) VccR and VccT are the receiver and transmitter power supplies. They are defined as 3.3V ±5% at the SFP connector pin. Maximum supply current is 300mA. Recommended host board power supply filtering is shown below. Inductors with DC resistance of less than 1 ohm should be used in order to maintain the required voltage at the SFP input pin with 3.3V supply voltage. When the recommended supply-filtering network is used, hot plugging of the SFP transceiver module will result in an inrush current of no more than 30mA greater than the steady state value. VccR and VccT may be internally connected within the SFP transceiver module.

8) TD-/+ : These are the differential transmitter inputs. They are AC-coupled, differential lines with 100Ω differential termination inside the module. The AC coupling is done inside the module and is thus not required on the host board. The inputs will accept differential swings of 500 – 2400 mV (250 – 1200mV single-ended), though it is recommended that values between 500 and 1200 mV differential (250 – 600mV single-ended) be used for best EMI performance.

EEPROM

The serial interface uses the 2-wire serial CMOS EEPROM protocol defined for the ATMEL AT24C02/04 family of components. When the serial protocol is activated, the host generates the serial clock signal (SCL). The positive edge clocks data into those segments of the EEPROM that are not write protected within the SFP transceiver. The negative edge clocks data from the SFP transceiver. The serial data signal (SDA) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially.

The Module provides diagnostic information about the present operating conditions. The transceiver generates this diagnostic data by digitization of internal analog signals. Calibration and alarm/warning threshold data is written during device manufacture. Received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring all are implemented. The diagnostic data are raw A/D values and must be converted to real world units using calibration constants stored in EEPROM locations 56 – 95 at wire serial bus address A2h. The digital diagnostic memory map specific data field define as following .For detail EEPROM information, please refer to the related document of SFF 8472 Rev 9.3.



EEPROM Serial ID Memory Contents

Accessing Serial ID Memory uses the 2 wire address 1010000X (A0H). Memory Contents of Serial ID are shown in Table 1a and Table 1b.

Table 1a Serial ID Memory Contents

Addr.	Size (Bytes)	Name of Field	Hex	Description
BASE ID FIELDS				
0	1	Identifier	03	SFP
1	1	Ext. Identifier	04	SFP function is defined by serial ID only
2	1	Connector	07	LC Connector
3-10	8	Transceiver	00 XX XX 00 00 00 00 00	OC 3, Single mode inter. or long reach
11	1	Encoding	03	NRZ
12	1	BR, Nominal	02	155Mbps
13	1	Reserved	00	
14	1	Length (9µm)km	50	Transceiver transmit distance
15	1	Length(9µm)100m	FF	
16	1	Length (50µm) 10m	00	
17	1	Length(62.5µm)10m	00	
18	1	Length (Copper)	00	Not compliant
19	1	Reserved	00	
20-35	16	Vendor name	45 4F 50 54 4F 4C 49 4E 4B 20 20 20 20 20 20 20	ELOPTOLINK

36	1	Reserved	00	
37-39	3	Vendor OUI	XX XX XX ^(note9)	
40-55	16	Vendor PN	XX XX XX XX XX XX XX XX XX XX XX XX XX XX XX XX ^(note9)	
56-59	4	Vendor rev	XX XX XX XX ^(note9)	
60-61	2	Wavelength	05 1E/06 0E	1310nm/1550nm
62	1	Reserved	00	
63	1	CC_BASE	Check Sum (Variable)	Check code for Base ID Fields
EXTENDED ID FIELDS				
64-65	2	Options	00 1A	TX_DISABLE, TX_FAULT and Loss of Signal implemented.
66	1	BR,max	00	
67	1	BR,min	00	
68-83	16	Vendor SN	XX XX XX XX XX XX XX XX 20 20 20 20 20 20 20 20 ^(note9)	Serial Number of transceiver (ASCII). For example "B000822".
84-91	8	Date code	XX XX XX XX XX XX XX XX ^(note9)	Manufactory date code. For example "080405".
92	1	Diagnostic Monitoring Type	XX ^(note9)	Digital diagnostic monitoring implemented
93	1	Enhanced Options	XX ^(note9)	Optional flags
94	1	SFF_8472 Compliance	XX ^(note9)	01 for diagnostics (Rev9.3 SFF-8472).
95	1	CC_EXT	Check Sum (Variable)	Check sum for Extended ID Field.
VENDOR SPECIFIC ID FIELDS				
96-127	32	Vendor Specific	Read only	Depends on customer information
128-255	128	Reserved	Read only	

Note9: The "XX" byte should be filled in according to practical case. For more information, please refer to the related document of SFP Multi-Source Agreement (MSA).

Digital Diagnostic Monitoring Interface (2-Wire Address A2H)

Alarm and Warning Thresholds (2 Wire Address A2H)

Address	# Bytes	Name of Field	Real Value	Unit
00-01	2	Temp High Alarm	110	Degree C
02-03	2	Temp Low Alarm	-45	Degree C
04-05	2	Temp High Warning	95	Degree C
06-07	2	Temp Low Warning	-42	Degree C

08-09	2	Voltage High Alarm	3.6	V
10-11	2	Voltage Low Alarm	2.9	V
12-13	2	Voltage High Warning	3.5	V
14-15	2	Voltage Low Warning	3.0	V
16-17	2	Bias High Alarm	90	mA
18-19	2	Bias Low Alarm	2	mA
20-21	2	Bias High Warning	80	mA
22-23	2	Bias Low Warning	3	mA
24-25	2	TX Power High Alarm	4	dBm
26-27	2	TX Power Low Alarm	-3	dBm
28-29	2	TX Power High Warning	3	dBm
30-31	2	TX Power Low Warning	-2	dBm
32-33	2	RX Power High Alarm	-9	dBm
34-35	2	RX Power Low Alarm	-34.5	dBm
36-37	2	RX Power High Warning	-10	dBm
38-39	2	RX Power Low Warning	-33.5	dBm
40-55	16	Reserved	-	

Calibration Constants (2 Wire Address A2H)

Address	# Bytes	Name of Field	HEX	Description
56-59	4	RX_PWR (4)	00 00 00 00	Set to zero for “internally calibrated” devices.
60-63	4	RX_PWR (3)	00 00 00 00	Set to zero for “internally calibrated” devices.
64-67	4	RX_PWR (2)	00 00 00 00	Set to zero for “internally calibrated” devices.
68-71	4	RX_PWR (1)	3F 80 00 00	Set to 1 for “internally calibrated” devices.
72-75	4	RX_PWR (0)	00 00 00 00	Set to zero for “internally calibrated” devices.
76-77	2	TX_I (Slope)	01 00	Set to 1 for “internally calibrated” devices.
78-79	2	TX_I (Offset)	00 00	Set to zero for “internally calibrated” devices.
80-81	2	TX_PWR (Slope)	01 00	Set to 1 for “internally calibrated” devices.
82-83	2	TX_PWR (Offset)	00 00	Set to zero for “internally calibrated” devices.
84-85	2	T (Slope)	01 00	Set to 1 for “internally calibrated” devices.
86-87	2	T (Offset)	00 00	Set to zero for “internally calibrated” devices.
88-89	2	V (Slope)	01 00	Set to 1 for “internally calibrated” devices.
90-91	2	V (Offset)	00 00	Set to zero for “internally calibrated” devices.

92-94	3	Reserved	00 00 00	Reserved
95	1	Checksum	XX	Checksum of bytes 0 – 94.

A/D Value (2 Wire Address A2H)

Address	# Bytes	Name of Field	Description
96-97	2	Temperature (MSB, LSB)	Internally measured module temperature
98-99	2	Supply Voltage (MSB, LSB)	Internally measured supply voltage in module
100-101	2	TX Bias Current (MSB, LSB)	Internally measured TX Bias current
102-103	2	TX Optical Power (MSB, LSB)	Internally measured TX Optical Power
104-105	2	RX Received Power (MSB, LSB)	Measured RX input power
106-109	4	Reserved	

*Temperature (Signed twos complement value)

A2H Byte 96 (Temperature MSB)								A2H Byte 97 (Temperature LSB)							
S	2 ⁶	2 ⁵	2 ⁴	2 ³	2 ²	2 ¹	2 ⁰	2 ⁻¹	2 ⁻²	2 ⁻³	2 ⁻⁴	2 ⁻⁵	2 ⁻⁶	2 ⁻⁷	2 ⁻⁸

Supply Voltage, TX Bias Current, TX Optical Power, RX Received Power (Unsigned values)

A2H Byte 98 (Vcc MSB)								A2H Byte 99 (Vcc LSB)							
A2H Byte 100 (TX Bias MSB)								A2H Byte 101 (TX Bias LSB)							
A2H Byte 102 (TX Power MSB)								A2H Byte 103 (TX Power LSB)							
A2H Byte 104 (RX Power MSB)								A2H Byte 105 (RX Power LSB)							
2 ¹⁵	2 ¹⁴	2 ¹³	2 ¹²	2 ¹¹	2 ¹⁰	2 ⁹	2 ⁸	2 ⁷	2 ⁶	2 ⁵	2 ⁴	2 ³	2 ²	2 ¹	2 ⁰

The digital value conversions are updated every 13ms (nominal) or 20ms (max) in rotation. After getting digital value, each measurement could be obtained by multiplying digital value by corresponding LSB value:

Temperature = Temp (Digital Value) × LSB_{Temp} = Temp (Digital Value) × 1/256; when Temperature < 128

Temperature = Temp (Digital Value) × LSB_{Temp} = [Temp (Digital Value) × 1/256] - 256; when Temperature ≥ 128

V_{cc} = V_{cc}(Digital Value) × LSB_{Vcc} = V_{cc}(Digital Value) × 100μV

TX Bias Current = TX Bias Current (Digital Value) × LSB_{TX,Bias} = TX Bias Current (Digital Value) × 2μA

TX Power = TX Power (Digital Value) × LSB_{TXPower} = TX Power (Digital Value) × 0.1μW

RX Power = RX Power (Digital Value) × LSB_{RXPower} = RX Power (Digital Value) × 0.1μW

Status Bits and Alarm/Warning Flag Bits (2 Wire Address A2H)

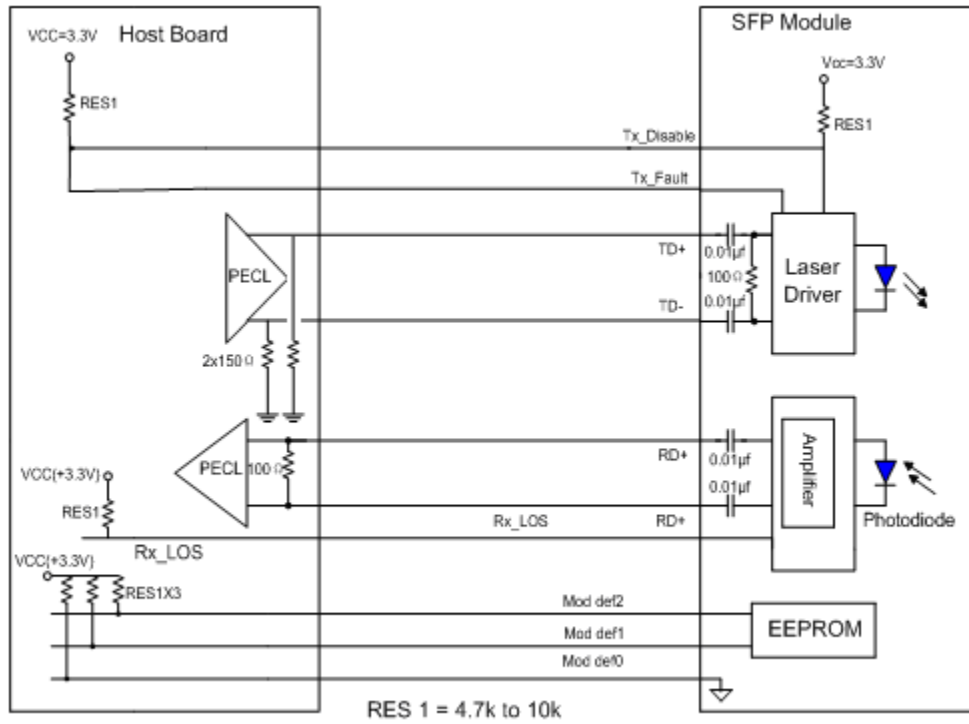
Address	Bit	Name	Description
110	7	TX Disable State	Digital state of TX disable (1) and enabled (0)
110	6	Soft TX Disable	Not implemented
110	5-3	Reserved	
110	2	TX Fault State	1=TX failure state, 0=TX normal state
110	1	LOS	Digital state of LOS output pin. 0=optical signal detected, 1=no optical signal detected
110	0	Data_Ready_Bar	Not implemented.
111	7-0	Reserved	Reserved
112	7	Temp High Alarm	Set when internal temperature exceeds high alarm

			level.
112	6	Temp Low Alarm	Set when internal temperature is below low alarm level.
112	5	Vcc High Alarm	Set when internal supply voltage exceeds high alarm level.
112	4	Vcc Low Alarm	Set when internal supply voltage is below low alarm level.
112	3	TX Bias High Alarm	Set when TX Bias current exceeds high alarm level.
112	2	TX Bias Low Alarm	Set when TX Bias current is below low alarm level.
112	1	TX Power High Alarm	Set when TX Power exceeds high alarm level.
112	0	TX Power Low Alarm	Set when TX Power is below low alarm level.
113	7	RX Power High Alarm	Set when Received Power exceeds high alarm level.
113	6	RX Power Low Alarm	Set when Received Power is below low alarm level.
113	5-0	Reserved Alarm	
114-115	All	Reserved	
116	7	Temp High Warning	Set when internal temperature exceeds high warning level.
116	6	Temp Low Warning	Set when internal temperature is below low warning level.
116	5	Vcc High Warning	Set when internal supply voltage > high warning level.
116	4	Vcc Low Warning	Set when internal supply voltage < low warning level.
116	3	TX Bias High Warning	Set when TX Bias current exceeds high warning level.
116	2	TX Bias Low Warning	Set when TX Bias current is below low warning level.
116	1	TX Power High Warning	Set when TX Power exceeds high warning level.
116	0	TX Power Low Warning	Set when TX Power is below low warning level.
117	7	RX Power High Warning	Set when Received Power exceeds high warning level.
117	6	RX Power Low Warning	Set when Received Power is below low warning level.
117	5-0	Reserved Warning	
118-119	All	Reserved	

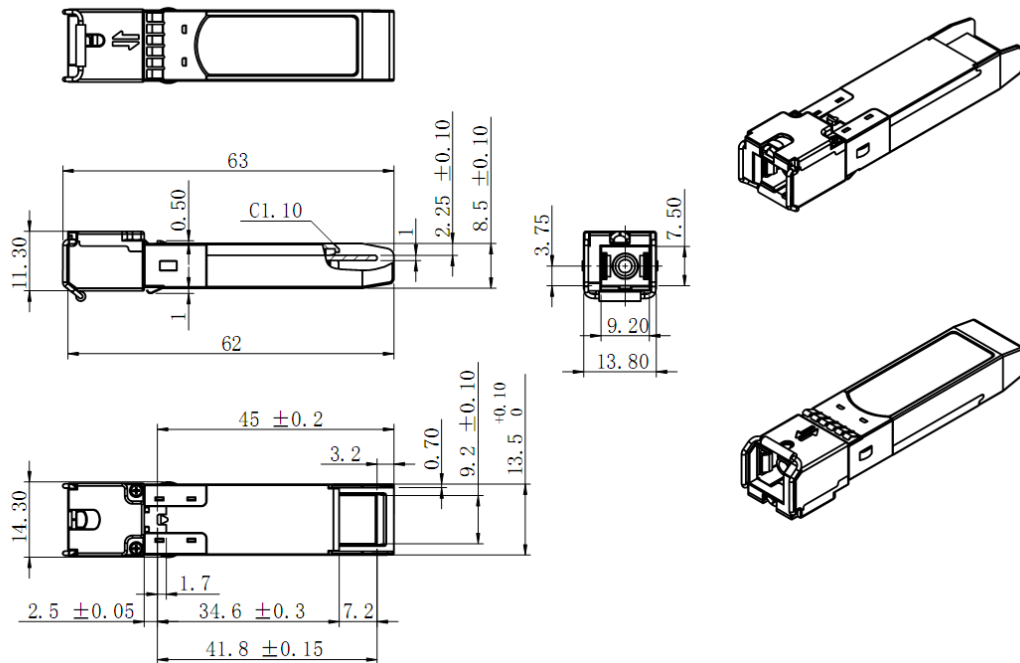
Vendor Specific and User Accessible EEPROM (2 Wire Address A2H)

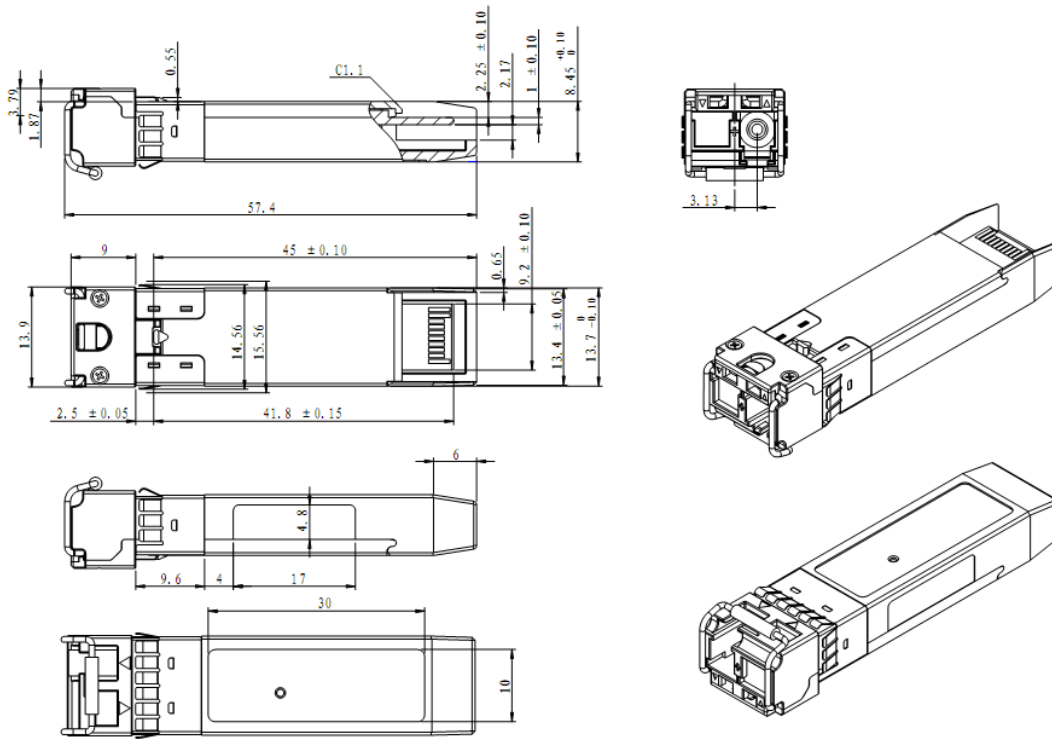
Address	# Bytes	Name	Description
120~122	3	Reserved	
123-127	5	Vendor Specific	
128-247	120	User writable EEPROM	
248-255	8	Vendor Specific	

Recommend Circuit Schematic



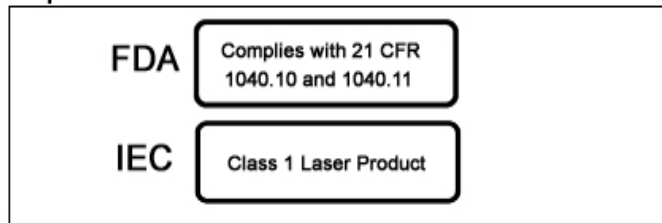
Mechanical Specifications





LC

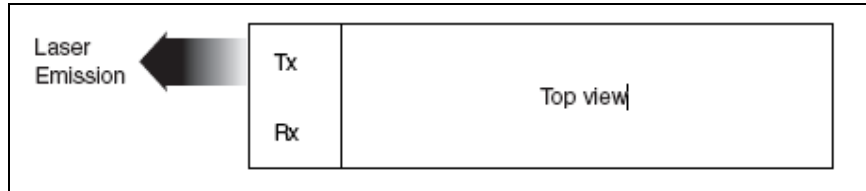
Class 1 Labels



Laser Emission Data

Wavelength	1310nm
Total output power (as defined by FDA: 7 mm aperture at 20 cm distance)	<0.195mW
Total output power (as defined by IEC: 7 mm aperture at 10 cm distance)	<15.6mW
Beam divergence	12.5°
Wavelength	1550nm
Total output power (as defined by FDA: 7 mm aperture at 20 cm distance)	<0.79mW
Total output power (as defined by IEC: 7 mm aperture at 10 cm distance)	<10mW
Beam divergence	12.5°

Laser Emission



Obtaining Document

You can visit our website:

<http://www.eoptolink.com>

Or contact Eoptolink Technology Inc., Ltd. Listed at the end of the documentation to get the latest documents.

Revision History

Revision	Initiate	Review	Approve	Revision History	Data
V1.a	Cathy	Kelly		Released.	2009.09.10
V1.b	Cathy	Kelly		Update the mechanical spec	2010.1.23

Notice:

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