

## EOL2-1696-X-X Series

### DWDM 10Gbit/s X2 Transceiver

### RoHS6 Compliant

#### Features

- ◆ Compatible with X2 MSA Rev.2.0b
- ◆ Support of IEEE 802.3ae 10GBASE-LR at 10.3125Gbit/s
- ◆ Compliance to Fiber Channel 1200-SM-LL-L at 10.51875Gbit/s
- ◆ Available in all 100 GHz C-Band Wavelengths on the DWDM ITU grid
- ◆ DWDM Rated EML Transmitter and PIN High Sensitivity Receiver
- ◆ Low Power Consumption 2.0 W (typ.)
- ◆ Case Temperature Range:  
Standard: -5°C - 70°C
- ◆ Laser Class 1 compliant
- ◆ Duplex SC connector
- ◆ Hot pluggable 70-pin connector with XAUI electrical interface
- ◆ Management and control via MDIO 2-wire interface
- ◆ Complaint with the EU RoHS 6 Environmental requirements



#### Applications

- ◆ 10G Ethernet

#### Ordering Information

Part No.	Data Rate	Link Budget	Interface	Temperature	DDM
EOL2-1696-X <sub>1</sub> <sup>*(note1)</sup> -X <sub>2</sub> <sup>*(note2)</sup>	10.51875Gbps	≥15dB	SC	Standard	YES

Note1: X<sub>1</sub> refers to link budget; standard version is EOLS-1696-15-X.

Note2: X<sub>2</sub> refers to DWDM wavelength definition in the following table.

**X- Channel refers to the following table:**

Channel	Part NO.	Frequency (THz)	Center Wavelength (nm)
17*	EOL2-1696-15-17	191.7	1563.86
18*	EOL2-1696-15-18	191.8	1563.05
19*	EOL2-1696-15-19	191.9	1562.23
20*	EOL2-1696-15-20	192.0	1561.42
21	EOL2-1696-15-21	192.1	1560.61

22	EOL2-1696-15-22	192.2	1559.79
23	EOL2-1696-15-23	192.3	1558.98
24	EOL2-1696-15-24	192.4	1558.17
25	EOL2-1696-15-25	192.5	1557.36
26	EOL2-1696-15-26	192.6	1556.55
27	EOL2-1696-15-27	192.7	1555.75
28	EOL2-1696-15-28	192.8	1554.94
29	EOL2-1696-15-29	192.9	1554.13
30	EOL2-1696-15-30	193.0	1553.33
31	EOL2-1696-15-31	193.1	1552.52
32	EOL2-1696-15-32	193.2	1551.72
33	EOL2-1696-15-33	193.3	1550.92
34	EOL2-1696-15-34	193.4	1550.12
35	EOL2-1696-15-35	193.5	1549.32
36	EOL2-1696-15-36	193.6	1548.51
37	EOL2-1696-15-37	193.7	1547.72
38	EOL2-1696-15-38	193.8	1546.92
39	EOL2-1696-15-39	193.9	1546.12
40	EOL2-1696-15-40	194.0	1545.32
41	EOL2-1696-15-41	194.1	1544.53
42	EOL2-1696-15-42	194.2	1543.73
43	EOL2-1696-15-43	194.3	1542.94
44	EOL2-1696-15-44	194.4	1542.14
45	EOL2-1696-15-45	194.5	1541.35
46	EOL2-1696-15-46	194.6	1540.56
47	EOL2-1696-15-47	194.7	1539.77
48	EOL2-1696-15-48	194.8	1538.98
49	EOL2-1696-15-49	194.9	1538.19
50	EOL2-1696-15-50	195.0	1537.40
51	EOL2-1696-15-51	195.1	1536.61
52	EOL2-1696-15-52	195.2	1535.82
53	EOL2-1696-15-53	195.3	1535.04
54	EOL2-1696-15-54	195.4	1534.25
55	EOL2-1696-15-55	195.5	1533.47
56	EOL2-1696-15-56	195.6	1532.68
57	EOL2-1696-15-57	195.7	1531.90
58	EOL2-1696-15-58	195.8	1531.12
59	EOL2-1696-15-59	195.9	1530.33
60*	EOL2-1696-15-60	196.0	1529.55
61*	EOL2-1696-15-61	196.1	1528.77

\*This channel is supported with limited availability; please contact Eoptolink for further details.

## Regulatory Compliance

Feature	Standard	Performance
Electrostatic Discharge (ESD) to the Electrical Pins	MIL-STD-883G Method 3015.7	Class 1C (>1000V)
Electrostatic Discharge to the Enclosure	EN 55024:1998+A1+A2 IEC-61000-4-2 GR-1089-CORE	Compliant with standards
Electromagnetic Interference (EMI)	FCC Part 15 Class B EN55022:2006 CISPR 22B :2006 VCCI Class B	Compliant with standards Noise frequency range: 30MHz to 6GHz. Good system EMI design practice required to achieve Class B margins. System margins are dependent on customer host board and chassis design.
Immunity	EN 55024:1998+A1+A2 IEC 61000-4-3	Compliant with standards. 1KHz sine-wave, 80% AM, from 80MHz to 1GHz. No effect on transmitter/receiver performance is detectable between these limits.
Laser Eye Safety	FDA 21CFR 1040.10 and 1040.11 EN (IEC) 60825-1:2007 EN (IEC) 60825-2:2004+A1	CDRH compliant and Class I laser product. TüV Certificate No. 50135086
Component Recognition	UL and CUL EN60950-1:2006	UL file E317337 TüV Certificate No. 50135086 (CB scheme )
RoHS6	2002/95/EC 4.1&4.2 2005/747/EC 5&7&13	Compliant with standards <sup>*note3</sup>

Note3: For update of the equipments and strict control of raw materials, EOPTOLINK has the ability to supply the customized products since Jan 1, 2007, which meet the requirements of RoHS6 (Restrictions on use of certain Hazardous Substances) of European Union.

In light of item 5 in RoHS exemption list of RoHS Directive 2002/95/EC, Item 5: Lead in glass of cathode ray tubes, electronic components and fluorescent tubes.

In light of item 13 in RoHS exemption list of RoHS Directive 2005/747/EC, Item13: Lead and cadmium in optical and filter glass. The three exemptions are being concerned for Eoptolink's transceivers, because Eoptolink's transceivers use glass, which may contain Pb, for components such as lenses, windows, isolators, and other electronic components.

## Description

The EOL2-1696-X-X Series is highly integrated, serial optical transponder module for high-speed,

10Gbit/s data transmission applications. The module is fully compliant to IEEE 802.3ae standard for Ethernet, making it ideally suited for 10GbE datacom (rack to-rack, client interconnection) applications. The transponder module comprises a transmitter with cooled 1550nm EML laser, a receiver with a PIN photodiode, a XAUI-Attachment Interface, an integrated Coder /Decoder and multiplexer / de-multiplexer (SERDES: Serializer/Deserializer).

The transponder operates within a wide case temperature range of -5°C to +70°C and offers optimum heat dissipation and excellent electromagnetic shielding which enables high port densities for 10GbE systems. A 70 pin electrical connector and a duplex SC connector optical interface assure that connectivity is compliant to the X2 and XENPAK MSA.

The transmitter section uses a DWDM multiple quantum well EML laser and is a class 1 laser compliant according to International Safety Standard IEC-60825. The receiver section uses an integrated InGaAs detector preamplifier (IDP) mounted in an optical header and a limiting post-amplifier IC.

Digital diagnostic monitoring (DDM) is implemented and fully compliant with X2 DDM architecture. The unit monitors temperature, receive optical power, transmit optical power, and laser bias current.

### Absolute Maximum Ratings\*

Rating	Symbol	Min	Max	Units
Storage Ambient Temperature	T <sub>s</sub>	-40	+85	°C
Powered Case Temperature	T <sub>c</sub>	-5	+70	°C
Supply Voltage 3.3V	V <sub>CC3</sub>	-0.3	4.0	V
Supply Voltage 5V	V <sub>CC5</sub>	-0.5	7.0	V
Input Voltage Low Speed Signals	V <sub>I</sub>	-0.5	3.3	V
Clamp Currents Low Speed Signals	I <sub>IK</sub>	-50		mA
XAUI Input Level	V <sub>IXAUI</sub>	-0.4	2.3	V
Differential XAUI Input Amplitude	V <sub>IDXAUI</sub>		1000	mV
Optical Receiver Input Power	P <sub>Rx</sub>		+1.5	dBm
Static Discharge Voltage			500	V

\*Any stress beyond the maximum ratings can result in permanent damage permanently. The device specifications are guaranteed only under the recommended operating conditions.

### Recommended Operating Conditions

Parameter	Symbol	Min	Typ	Max	Units
Operating Case Temperature Range	T <sub>c</sub>	-5		+70	°C
Operating Humidity		8		80	%
Power Supply Voltage @ 3.3V	V <sub>CC3</sub>	3.135	3.3	3.465	V
Power Supply Voltage @ 5.0V	V <sub>CC5</sub>	4.75	5.00	5.25	V

## Electrical Characteristics

Parameter	Symbol	Min	Typ	Max	Units
5V Supply Current	$I_{VCC5}$	0.38	0.7	3.5	mA
3.3V Supply Current	$I_{VCC3}$	310	360	576	mA

## XAUI Input Characteristics <sup>\*Note4</sup>

Parameter	Conditions	Symbol	Min	Typ	Max	Units
Data Rate	10GBASE-LR module			3.125		GBd
	1200-SM-LL-L module			3.1875		GBd
	Relative Tolerance		-100		+100	ppm
Differential Input Voltage Swing	8B/10B Coded Input Signal	$V_{ID}$	175		2,000	mV <sub>P-P</sub>
Differential Return Loss	100MHz — 2.5GHz	SDD11	10			dB
Common Mode Return Loss	100MHz — 2.5GHz	SCC11	6			dB
Total Peak-to-Peak Jitter Tolerance	Sinusoidal Jitter @ 0~20MHz	$T_{JRDS}$	0.32			UI
Differential Input Impedance		$R_{IND}$	80	100	120	$\Omega$

Note4: XAUI-input-Lanes are ac-inputs.

## XAUI Output Characteristics <sup>\*Note5</sup>

Parameter	Conditions	Symbol	Min	Typ	Max	Units
XAUI Data Rate	10GBASE-LR module			3.125		GBd
	1200-SM-LL-L module			3.1875		GBd
	XAUI Data Rate Tolerance		-100		+100	ppm
Differential Output Voltage Swing	$R_{LOAD}=100 \pm 5\%$	$V_{out}$	800		1,600	mV <sub>P-P</sub>
Differential Output Impedance		$Z_{out}$	80	100	120	$\Omega$
Differential Transition Time	20% - 80%	tr/tf	50		130	ps
Total Output Jitter					0.17	UI
Total Deterministic Output Jitter					0.08	UI

Note5: XAUI-output-Lanes are ac-outputs.

## Optical Interface

### Recommended Operating Conditions

Parameter	Remark	Symbol	Min	Typ	Max	Units
Input Data Rate	10GBASE-LR module	DRo		10.3125		Gbd
	1200-SM-LL-L module			10.51875		GBd
	relative tolerance	ADR/DR <sub>typ</sub>	-100		+100	ppm

### Transmitter Characteristics

(If not otherwise mentioned under recommended operating conditions and standard compliant single mode fiber.)

Characteristic	Conditions	Symbol	Min	Typ	Max	Units
Data Rate	10GBASE-LR module			10.3125		Gbps
	1200-SM-LL-L module			10.51875		Gbps
	Relative tolerance to nominal DR	ADR/DR <sub>typ</sub>	-100		100	ppm
SMSR		SMSR	30			dB
Optical Output Power		P <sub>out</sub>	-1		3	dBm
Extinction Ratio	EOL	ER	9			dB
Optical Modulation Amplitude		OMA	500			pW
Transmitter Penalty	Bessel-Thompson Filter	TP			3	dB
Overshoot	Bessel-Thompson Filter	O <sub>s</sub>			40	%

### Receiver Characteristics <sup>Note6</sup>

(If not otherwise mentioned under recommended operating conditions and standard compliant single mode fiber.)

Parameter	Conditions	Symbol	Min	Typ	Max	Units
Data Rate	10GBASE-LR module			10.3125		Gbps
	1200-SM-LL-L module			10.51875		Gbps
	Relative tolerance to nominal DR	ADR/DR <sub>typ</sub>	-100		100	ppm
Receiver Sensitivity	BER 10 <sup>-12</sup> @2 <sup>31</sup> -1	P <sub>min</sub>			-16	dBm
Receiver Overload	BER 10 <sup>-12</sup> @2 <sup>31</sup> -1	P <sub>max</sub>			-1.5	dBm
Stressed Receiver Sensitivity	OMA	P <sub>min</sub>			-12.6	dBm
Saturation Input Power		P <sub>max</sub>	0.5			dBm

Note6: The specified characteristics are met within the recommended range of operating conditions and under the default settings of output power and modulation amplitude. Changing the settings of the optical output power will affect the dynamic behavior of the output signal. Unless otherwise noted, typical data is quoted at nominal voltages and +25°C ambient

temperature.

## MDIO Interface

### DC Characteristics

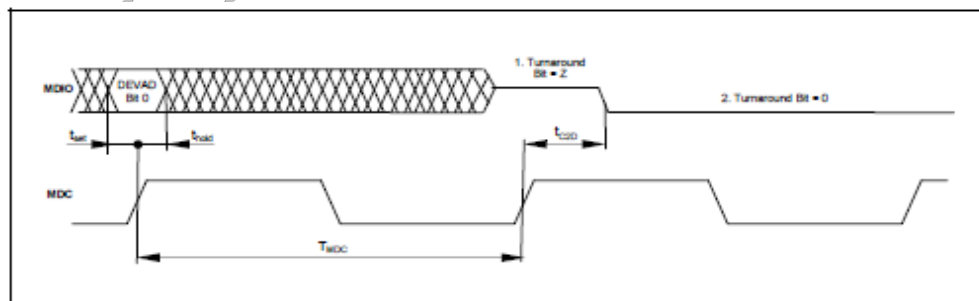
(If not otherwise mentioned under the recommended operating conditions.)

Characteristic	Condition	Symbol	Minimum	Maximum	Unit
Input high voltage		$V_H$	0.84	1.5	V
Input low voltage		$V_L$	-0.3	0.36	V
MDIO Input current	MMD Driver in tri-state	$I_{MDIO}$	-10	8	pA
MDC Input current		$I_{MDC}$	-5	5	pA
Output low voltage	$I_{OL} = 100\text{pA}$	$V_{QL}$	-0.3		V
	$I_{OL} = 4\text{mA}$	$V_{QL}$		0.2	V
Output high voltage	$R_{PULL-Up} = 357\Omega \pm 1\%$ $V_{PULL-Up} = 1.14 \sim 1.5\text{V}$	$V_{QH}$	1.136	1.5	V
Output low current	$V_I = 0.2\text{V}$	$I_{QL}$	+4		mA
MDIO Input capacitance	$V_I = 0 \sim 1.5\text{V}$	$C_{in}$		10	pF

### AC Characteristics

If not otherwise mentioned under the recommended operating conditions. Furthermore it's recommended that clock period time  $T_{MDC}$  is not less than 310ns and the sum of input currents of loads on the bus does not exceed 256pA at high and at low not below -320pA.

Characteristic	Condition	Symbol	Minimum	Maximum	Unit
Set Up Time		$t_{set}$		10	ns
Hold Time		$t_{hold}$		10	ns
Clock to Data Time	$R_{PULL-Up} = 357\Omega \pm 1\%$ , $C_{BUS} \leq 470\text{pF}$	$t_{C2D}$	0	300	ns



Example Timing Diagram: Turnover Timing at Read Cycle

## Electro Static Discharge (ESD)

The maximum electrostatic charge based on a human body model and the conditions as outlined below is:

Parameter	Conditions	Symbol	Min	Typ	Max	Units
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Static Discharge Voltage	MIL STD 883 Method				500	V
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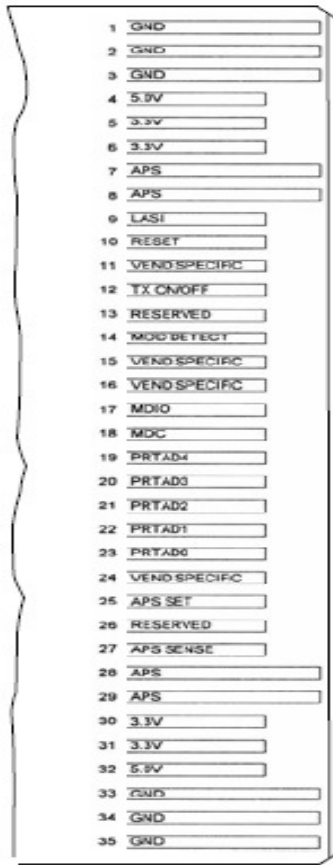
## Thermal Management

The transponder is designed for an operation within a case temperature range between -5 to +70°C at an altitude of < 3km. The built in heatsink provides an optimized thermal performance. The user needs to guarantee per system design not to exceed this temperature range. It has to be considered that in case of usage of multiple modules on a single hostboard that there is a temperature rise among the modules hosted side by side (see figure below). Airflow direction and air speed needs to be chosen accordingly. For further information it is referred to the MSA document.

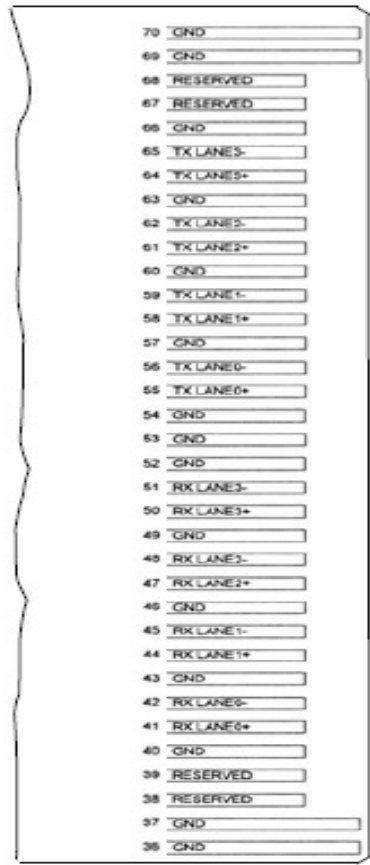
## DDM Monitoring Specification

Parameter	Range	Accuracy	Calibration
Temperature	-10 to +80°C	± 3°C	Internal
Bias Current	0 to 100mA	±10%	Internal
TX Power	-1 to +3dBm	± 3dB	Internal
RX Power	-17 to -0.5dBm	± 3dB	Internal

## Card-Edge-Connector-Pinning and Layout



Top view -Top side of PCB



Top view -Bottom side of PCB

### Electrical Pin Definition

Symbol	Logic	PIN	Name/Description	Note
3.3V		5, 6, 30, 31	Power Supply of Optical Receiver and Transmitter and Control Circuits	2
5.0V		4, 32	Power Supply of Optical Receiver Frontend	2
APS		7, 8, 28, 29	Adaptive Power Supply, Supply of PHY XS and PCS Layer Devices	2
APS SENSE		27	APS Sense Output for APS Control Circuit	
APS SET		25	Feedback Input for APS, Input of APS Setting Resistor	
GND		1, 2, 3, 33,34, 35, 36, 37, 40, 43,46, 49, 52, 53, 54, 57,60, 63, 66, 69, 70	Common Electrical Ground	1
LASI	1.2V CMOS	9	Link Alarm Status Interrupt, low active, Open Drain Output	

	Open Drain Output		Supposed to operate with 10KΩ - 22KΩ pull upon host. Logic High: Normal Operation Logic Low: Link Alarm is indicated	
MDC		18	Management Clock Input	3
MDIO		17	Management Data IO	3
MOD DETECT		14	1kΩ to Ground for APS Circuit Environment	
PRTADO	1.2V CMOS Input	23	Port Address Bit 0 (Low = 0), internally pulled up by 18kΩ	
PRTAD1	1.2V CMOS Input	22	Port Address Bit 1 (Low = 0), internally pulled up by 18kΩ	
PRTAD2	1.2V CMOS Input	21	Port Address Bit 2 (Low = 0), internally pulled up by 18kΩ	
PRTAD3	1.2V CMOS Input	20	Port Address Bit 3 (Low = 0), internally pulled up by 18kΩ	
PRTAD4	1.2V CMOS Input	19	Port Address Bit 4 (Low = 0), internally pulled up by 18kΩ	
RESERVED		13, 38, 39, 67, 68	Reserved by MSA, internally not connected	
RESERVED		26	Reserved for Avalanche Photodiode use, internally not connected	5
RESET	1.2V CMOS Input	10	Low active Reset Input 10KΩ pull-up on Transceiver Logic high = Normal Operation Logic Low = Reset asserted	
TX ON/OFF	1.2V CMOS Input	12	High active Transmitter Enable Input 10KΩ pull-up on Transceiver Logic high = Transmitter active (normal Operation) And Register Bit 1.9.0 set to low as well Logic Low = shut down of Transmitter	
VENDSPECIFIC		11, 15, 16, 24	Vendor Specific Pin,. for proper operation leave unconnected	5

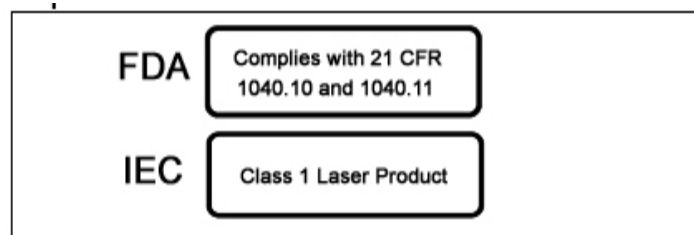
RX LANE0+		41	Module XAUI Output Lane 0+	4
RX LANE0-		42	Module XAUI Output Lane 0-	4
RXLANE1+		44	Module XAUI Output Lane 1+	4
RXLANE1-		45	Module XAUI Output Lane 1-	4
RX LANE2+		47	Module XAUI Output Lane 2+	4
RX LANE2-		48	Module XAUI Output Lane 2-	4
RX LANE3+		50	Module XAUI Output Lane 3+	4
RX LANE3-		51	Module XAUI Output Lane 3-	4
TX LANE0+		55	Module XAUI Input Lane 0+	4
TX LANE0-		56	Module XAUI Input Lane 0-	4
TXLANE1+		58	Module XAUI Input Lane 1+	4
TXLANE1-		59	Module XAUI Input Lane 1-	4
TX LANE2+		61	Module XAUI Input Lane 2+	4
TX LANE2-		62	Module XAUI Input Lane 2-	4
TX LANE3+		64	Module XAUI Input Lane 3+	4
TX LANE3-		65	Module XAUI Input Lane 3-	4

- 1) Ground connections are common for TX and RX.
- 2) Each connector contact is rated at 0.5A.
- 3) MDIO and MDC timing must comply with IEEE 802.3ae clause 45.3.
- 4) XAUI output characteristics comply with IEEE 802.3ae clause 47.
- 5) Transceivers will be MSA compliant when no signals are present on the vendor specific pins.

## Eye Safety

This laser based multimode transceiver is a Class **1** product. It complies with IEC 60825-1: 2007 and FDA performance standards for laser products (21 CFR 1040.10 and 1040.11) except for deviations pursuant to Laser Notice 50, dated June 24, 2007.

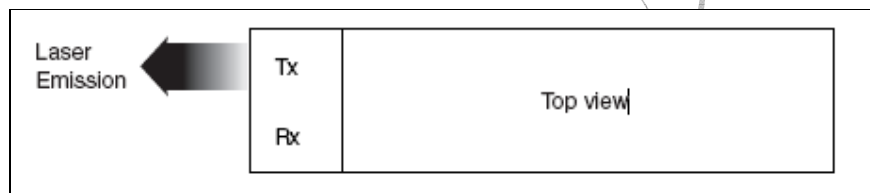
## Class 1 Labels



## Laser Emission Data

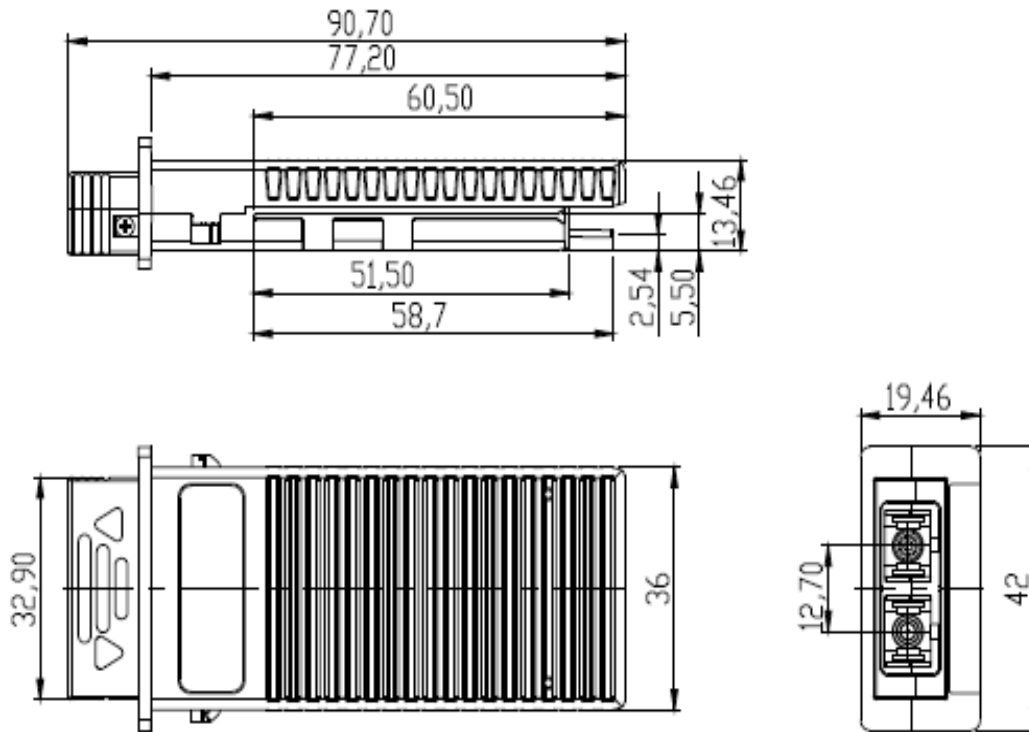
Wavelength	>1525nm
Total output power (as defined by IEC: 7mm aperture at 70mm distance)	15.6mW
Beam divergence	14°

## Laser Emission



Eoptolink

## Mechanical Drawing



### Obtaining Document

You can visit our website:

<http://www.eoptolink.com>

Or contact Eoptolink Technology Inc., Ltd. listed at the end of the documentation to get the latest documents.

### Revision History

Revision	Initiated	Reviewed	Approved	Revision History	Release Date
V1.a	Jacky cheng	Kelly Cao		Released.	July 9, 2009
V1.b	Kelly.Cao			Correct the connector type.	Dec 3, 2009
V1.c	Kelly.Cao			Updating the PN.	Apr 22, 2010

### Notice:

Eoptolink reserves the right to make changes to or discontinue any optical link product or service identified in this publication, without notice, in order to improve design and/or performance. Applications that are described herein for any of the optical link products are for illustrative purposes only. Eoptolink makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

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